

FIG. 1

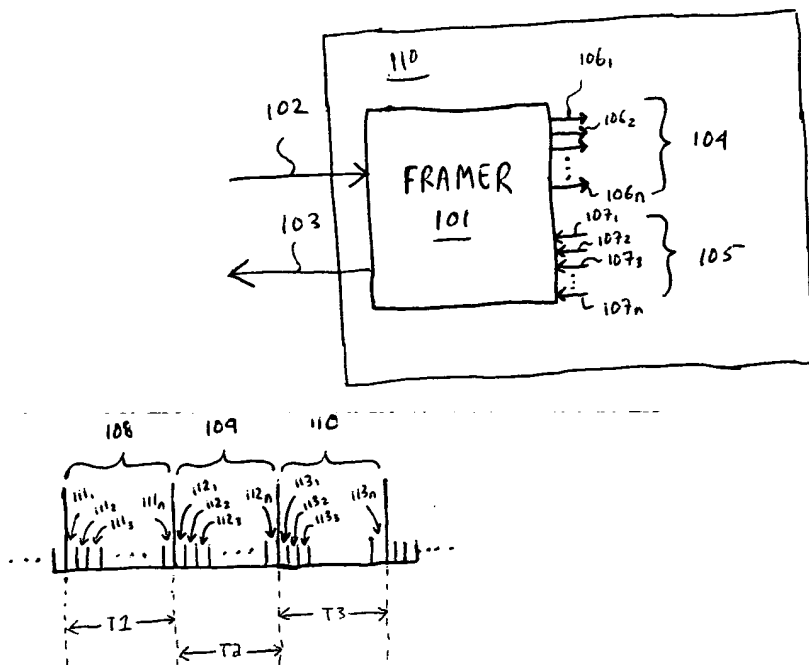


FIGURE 1

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Print Fig. 3

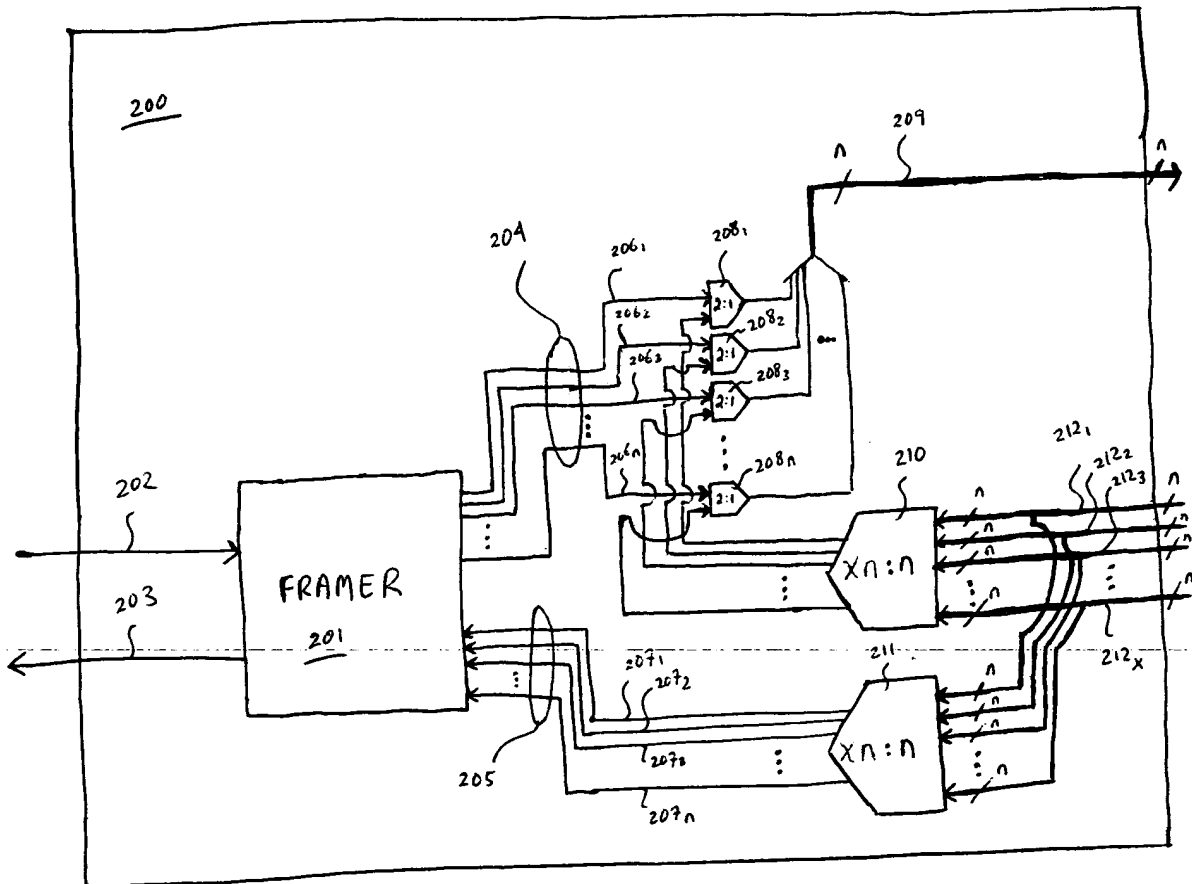
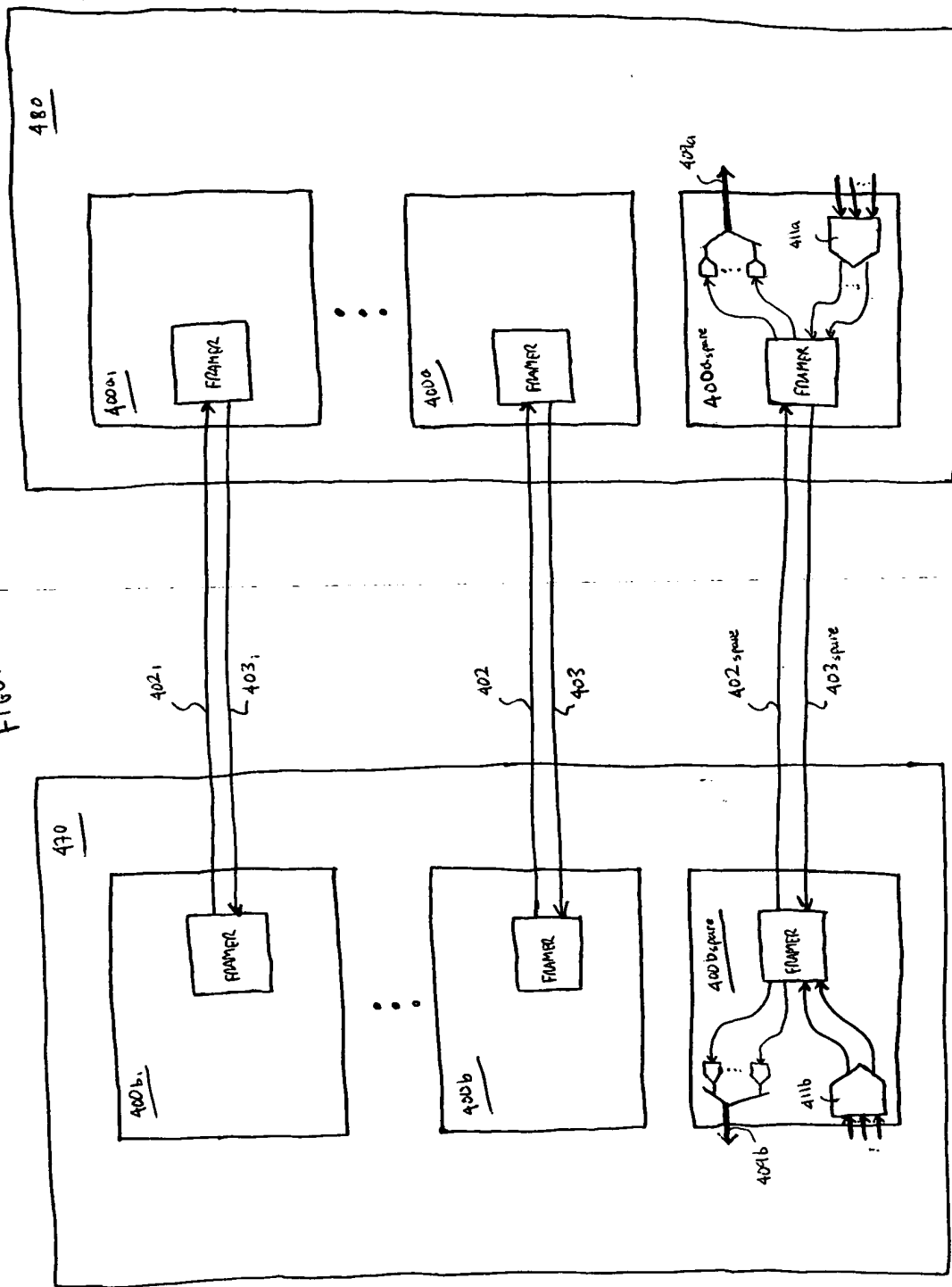


FIGURE 2

FIGURE 4



The diagram illustrates a network architecture with two main functional blocks: a **FRAMER** (201) and a **SWITCHING/ROUTING ENGINE** (501).

FRAMER (201): This block receives external inputs 202 and 203. It is connected to a series of multiplexers (206₁ to 206_n) via a bus 204. Each multiplexer 206_i has a 2:1 input and is connected to a corresponding multiplexer 208_i (also with a 2:1 input). The outputs of these multiplexers are connected to a bus 205. The FRAMER also has a direct output path 209.

SWITCHING/ROUTING ENGINE (501): This block receives inputs from the FRAMER via bus 205 and from an external source 502. It contains a series of multiplexers (505₁ to 505_n) connected to a bus 504. Each multiplexer 505_i has an X:n input and is connected to a corresponding multiplexer 503_i (also with an X:n input). The outputs of these multiplexers are connected to a bus 506 (506₁ to 506_n).

Interconnections: The output of the FRAMER (209) is connected to the input of the SWITCHING/ROUTING ENGINE (503). The output of the SWITCHING/ROUTING ENGINE (506) is connected to the input of the FRAMER (202). The output of the SWITCHING/ROUTING ENGINE (502) is connected to the input of the FRAMER (203).

Labels and Dimensions: The diagram includes various labels for components and buses, such as 202, 203, 204, 205, 206₁, 206₂, 206₃, 206_n, 208₁, 208₂, 208₃, 208_n, 209, 210, 211, 212₁, 212₂, 212₃, 212_x, 502, 503, 504, 505₁, 505₂, 505₃, 505_n, 506₁, 506₂, 506₃, 506_n, and 500. Dimensions are indicated by 'n' and 'X:n'.

FIGURE 5